

FORM PTO-1449 (REV. 7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. <b>501317.02 (30302/US)</b>		APPLICATION NO. <b>10/691,020</b>	
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>				APPLICANT(S) <b>Philip Neaves and Andrew Lever</b>			
				FILING DATE <b>October 21, 2003</b>		GROUP ART UNIT <b>2829</b>	

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
<i>DN</i>	AA	3,782,504	01/01/74	Billmaier et al.	187	29 R	<i>—</i>
<i>DN</i>	AB	4,280,221	07/21/81	Chun et al.	375	17	<i>—</i>
<i>DN</i>	AC	5,974,464	10/26/99	Shin et al.	709	231	<i>—</i>
<i>DN</i>	AD	6,690,309 B1	02/10/04	James et al.	341	102	<i>—</i>
<i>DN</i>	AE	6,859,883 B2	02/22/05	Svestka et al.	713	320	<i>—</i>
	AF						
	AG						
	AH						
	AI						
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FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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OTHER PRIOR ART <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>	
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EXAMINER  <i>[Signature]</i>	DATE CONSIDERED  <i>6/7/05</i>
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*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
TN	AA	6,285,201 B1	09/04/01	Farnworth et al.	324	755	
	AB						
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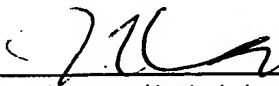
  

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DN	AA	5,673,130	09/30/97	Sundstrom et al.	359	158	—
DN	AB	6,188,232 B1	02/13/01	Akram et al.	324	755	—
DN	AC	6,310,494 B1	10/30/01	Ehben et al.	326	82	—
DN	AD	6,357,025 B1	03/12/02	Tuttle	714	724	—
DN	AE	6,368,930 B1	04/09/02	Enquist	438	320	—
DN	AF	6,396,292 B2	05/28/02	Hembree et al.	324	755	—
DN	AG	6,407,566 B1	06/18/02	Brunelle et al.	324	758	—
DN	AH	6,425,107 B1	07/23/02	Caldara et al.	714	759	—
DN	AI	6,490,188 B2	12/03/02	Nuxoll et al.	365	63	—
DN	AJ	6,563,133 B1	05/13/03	Tong	257	52	—
DN	AK	6,563,299 B1	05/13/03	Van Horn et al.	324	158.1	—
DN	AL	6,620,638 B1	09/16/03	Farrar	438	14	—
DN	AM	6,625,073 B1	09/23/03	Beffa	365	201	—
DN	AN	2001/0054908 A1	12/27/01	Farnworth et al.	324	755	—
	AO						


## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
DN	AP	0 277 764 A3	01/27/88	EP	—	—	X	
DN	AQ	0 366 263 A3	05/02/90	EP	—	—	X	
DN	AR	0 492 806 A3	11/26/91	EP	—	—	X	
DN	AS	0 805 356 A3	04/22/98	EP	—	—	X	
DN	AT	2 353 401 A	02/21/01	GB	—	—	X	
DN	AU	2 353 402 A	02/21/01	GB	—	—	X	
DN	AV	WO 0215185 A1	02/21/02	PCT	—	—	X	

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TN	AW	"International Technology Roadmap for Semiconductors", Assembly and Packaging, 2001, pp. 1-21.			
TN	AX	Karnezos, M. et al., "System in a Package (SiP) Benefits and Technical Issues", in <i>Proceedings of APEX</i> , San Diego, California, 2002, 7 pages.			
TN	AY	Mick, S. et al., "4Gbps High-Density AC Coupled Interconnection", Department of Electrical and Computer Engineering North Carolina State University, IEEE Custom Integrated Circuits Conference, May 12-16, 2002, pp. 133-140.			
TN	AZ	"Rapidly Advancing System-in-Package Fabrication Technology", Vol. 20, No.3, 2002, pp. 3-11.			
TN	BA	Scanlan, C.M. et al., "System-In-Package Technology, Application and Trends", 2001 Proceedings of SMTA International, Rosemont, Illinois, pp. 764-773.			
TN	BB	Wang, M. et al., "Configurable Area-IO Memory for System-In-A-Package (SiP)", 27 <sup>th</sup> European Solid-State Circuits Conference, September, 2001, 4 pages.			
	BC				
	BD				
	BE				
	BF				
	BG				
	BH				
	BI				
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TN	AA 5,786,979	07/28/98	Douglass	361	328	—
TN	AB 5,818,112	10/06/98	Weber et al.	257	777	—
TN	AC 6,496,889 B1	12/17/02	Perino et al.	710	110	—
TN	AD 6,500,696 B2	12/31/02	Sutherland	438	109	—
TN	AE 2001/0039075 A1	11/08/01	Doyle et al.	438	107	—
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